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Control Flow

Refinement and Simulation

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#### **Abstract Machines**

Thomas Sewell UNSW Term 3 2024

Refinement and Simulation

# Big O

We all know that MERGESORT has  $\mathcal{O}(n \log n)$  time complexity, and that BUBBLESORT has  $\mathcal{O}(n^2)$  time complexity, but what does that actually mean?

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# Big O

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#### **Big O Notation**

Given functions  $f, g : \mathbb{R} \to \mathbb{R}$ ,  $f \in \mathcal{O}(g)$  if and only if there exists a value  $x_0 \in \mathbb{R}$  and a coefficient *m* such that:

$$\forall x > x_0. \ f(x) \leq m \cdot g(x)$$

When analysing algorithms, we don't usually time how long they take to run on a real machine.

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# Big O

Q: How would you derive the complexity of this mergesort?

```
mergesort([]) = []
mergesort(xs) =
let (ys, zs) = partition xs;
    ys' = mergesort ys;
    zs' = mergesort zs
in merge ys' zs'
```

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# Big O

Q: How would you derive the complexity of this mergesort?

 $\begin{array}{ll} \operatorname{mergesort}([]) = [] & f(0) = c_1 \\ \operatorname{mergesort}(xs) = & f(n) = \\ & \operatorname{let}(ys,zs) = \operatorname{partition} xs; & c_2 * n + \\ & ys' = \operatorname{mergesort} ys; & f(n/2) + \\ & zs' = \operatorname{mergesort} zs & f(n/2) + \\ & \operatorname{in} \operatorname{merge} ys' zs' & c_3 * n \end{array}$ 

A: Define a cost function f, then find its closed form.

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Q: Is there a formal connection between mergesort and f, or did we just pull f out of thin air?

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A: Define a cost function f, then find its closed form.

Q: Is there a formal connection between mergesort and f, or did we just pull f out of thin air?

A: Well, um.

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#### **Cost Models**

A *cost model* is a mathematical model that measures the cost of executing a program. There are *denotational* cost models, that assign a cost directly to

syntax:

```
\llbracket \cdot \rrbracket : \operatorname{Program} \to \operatorname{Cost}
```

In this course, we will focus on operational cost models.

#### **Operational Cost Models**

First, we define a program-evaluating *abstract machine*. We determine the time cost by counting the number of steps it takes.

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### **Abstract Machines**

Abstract Machines

An abstract machine consists of:

- **1** A set of states  $\Sigma$ ,
- **2** A set of initial states  $I \subseteq \Sigma$ ,
- **3** A set of final states  $F \subseteq \Sigma$ , and
- A transition relation  $\mapsto \subseteq \Sigma \times \Sigma$ .

We've seen this before in structured operational (or small-step) semantics.

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#### The M Machine

Is just our usual small-step rules:

 $e_1 \mapsto_M e'_1$ (Plus  $e_1 e_2$ )  $\mapsto_M$  (Plus  $e'_1 e_2$ )  $e_1 \mapsto_M e'_1$  $(If e_1 e_2 e_3) \mapsto_M (If e'_1 e_2 e_3)$ (If (Lit True)  $e_2 e_3$ )  $\mapsto_M e_2$  (If (Lit False)  $e_2 e_3$ )  $\mapsto_M e_3$  $e_1 \mapsto_M e'_1$ (Apply  $e_1 e_2$ )  $\mapsto_M$  (Apply  $e'_1 e_2$ )  $e_2 \mapsto_M e'_2$ (Apply (Recfun (f.x. e))  $e_2$ )  $\mapsto_M$  (Apply (Recfun (f.x. e))  $e'_2$ )  $v \in F$  $(\text{Apply}(\text{Recfun}(f.x. e)) \lor) \mapsto_M e[x := v, f := (\text{Recfun}(f.x. e))]$ 

The M Machine is unsuitable as a basis for a cost model. Why?

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### Performance

One step in our machine should always only be  $\mathcal{O}(1)$  in our language implementation. Otherwise, counting steps will not get an accurate description of the time cost.

This makes for two potential problems:

Substitution occurs in function application, which is potentially  $\mathcal{O}(n)$  time.

# Performance

One step in our machine should always only be  $\mathcal{O}(1)$  in our language implementation. Otherwise, counting steps will not get an accurate description of the time cost.

This makes for two potential problems:

- Substitution occurs in function application, which is potentially  $\mathcal{O}(n)$  time.
- Control Flow is not explicit which subexpression to reduce is found by recursively descending the abstract syntax tree each time.

eval (Num n) = neval e = eval (oneStep e)

oneStep (Plus (Num n) (Num m)) = Num (n + m) oneStep (Plus (Num n)  $e_2$ ) = Plus (Num n) ( $oneStep \ e_2$ ) oneStep (Plus  $e_1 \ e_2$ ) = Plus ( $oneStep \ e_1$ )  $e_2$ 

. . .

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## The C Machine

We want to define a machine where all the rules are axioms, so there can be no recursive descent into subexpressions. How is recursion typically implemented?

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# The C Machine

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Stacks!

	f Frame	s Stack
Stack	$f \triangleright s$	Stack

**Key Idea**: States will consist of a current expression to evaluate and a stack of computational contexts that situate it in the overall computation. An example stack would be:

```
(\texttt{Plus 3} \Box) \triangleright (\texttt{Times} \Box (\texttt{Num 2})) \triangleright \circ
```

This represents the computational context:

 $(\texttt{Times}(\texttt{Plus} \ \exists \ \Box)(\texttt{Num} \ 2))$ 

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# The C Machine

Our states will consist of two modes:

- **Q** Evaluate the current expression within stack s, written  $s \succ e$ .
- **2** Return a value v (either a function, integer, or boolean) back into the context in s, written  $s \prec v$ .

Initial states start evaluation with an empty stack, i.e.  $\circ \succ e$ . Final states return a value to the empty stack, i.e.  $\circ \prec v$ .

Stack frames are expressions with holes or values in them:

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Stack frames are expressions with holes or values in them:

 $\frac{e_2 \text{ Expr}}{(\text{Plus } \Box e_2) \text{ Frame}} \quad \frac{v_1 \text{ Value}}{(\text{Plus } v_1 \Box) \text{ Frame}}$ 

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. . .

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# **Evaluating**

There are three axioms about Plus now:

When evaluating a Plus expression, first evaluate the LHS:

 $s \succ (\texttt{Plus } e_1 \ e_2) \quad \mapsto_C \quad (\texttt{Plus } \Box \ e_2) \triangleright s \succ e_1$ 



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Once the RHS is evaluated, return the sum:

 $(\text{Plus } v_1 \Box) \triangleright s \prec v_2 \quad \mapsto_C \quad s \prec v_1 + v_2$ 

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We also have a single rule about Num that just returns the value:

 $s \succ (\operatorname{Num} n) \mapsto_C s \prec n$ 

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## Example

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# Example

# $\circ \succ (\text{Plus (Plus (Num 2) (Num 3)) (Num 4)})$ $\mapsto_C \quad (\text{Plus } \Box (\text{Num 4})) \triangleright \circ \succ (\text{Plus (Num 2) (Num 3)})$

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#### Example

- $\mapsto_{\mathcal{C}} (\texttt{Plus} \Box (\texttt{Num 4})) \triangleright \circ \succ (\texttt{Plus} (\texttt{Num 2}) (\texttt{Num 3}))$
- $\mapsto_{\mathcal{C}} (\texttt{Plus} \Box (\texttt{Num 3})) \triangleright (\texttt{Plus} \Box (\texttt{Num 4})) \triangleright \circ \succ (\texttt{Num 2})$

Refinement and Simulation

#### Example

- $\mapsto_{\mathcal{C}} (\texttt{Plus} \Box (\texttt{Num 4})) \triangleright \circ \succ (\texttt{Plus} (\texttt{Num 2}) (\texttt{Num 3}))$
- $\mapsto_{\mathcal{C}}$  (Plus  $\Box$  (Num 3))  $\triangleright$  (Plus  $\Box$  (Num 4))  $\triangleright \circ \succ$  (Num 2)
- $\mapsto_C$  (Plus  $\square$  (Num 3))  $\triangleright$  (Plus  $\square$  (Num 4))  $\triangleright \circ \prec 2$

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## Example

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- $\mapsto_{\mathcal{C}} (\texttt{Plus 2} \Box) \triangleright (\texttt{Plus} \Box (\texttt{Num 4})) \triangleright \circ \succ (\texttt{Num 3})$

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- $\mapsto_{\mathcal{C}} (\texttt{Plus} \Box (\texttt{Num 4})) \triangleright \circ \succ (\texttt{Plus} (\texttt{Num 2}) (\texttt{Num 3}))$
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- $\mapsto_C \quad (\texttt{Plus 2} \Box) \triangleright (\texttt{Plus} \Box (\texttt{Num 4})) \triangleright \circ \succ (\texttt{Num 3})$
- $\mapsto_{\mathcal{C}} (\text{Plus } 2 \Box) \triangleright (\text{Plus } \Box (\text{Num } 4)) \triangleright \circ \prec 3$

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## Example

 $\circ \succ (Plus (Plus (Num 2) (Num 3)) (Num 4))$ 

- $\mapsto_{\mathcal{C}} (\texttt{Plus} \Box (\texttt{Num 4})) \triangleright \circ \succ (\texttt{Plus} (\texttt{Num 2}) (\texttt{Num 3}))$
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- $\mapsto_C$  (Plus  $\square$  (Num 4))  $\triangleright \circ \prec 5$
- $\mapsto_C$  (Plus 5  $\Box$ )  $\triangleright \circ \succ$  (Num 4)
- $\mapsto_C$  (Plus 5  $\square$ )  $\triangleright \circ \prec 4$

 $\mapsto_{\mathcal{C}} \circ \prec 9$ 

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### **Other Rules**

We have similar rules for the other operators and for booleans. For If:

 $s\succ (\texttt{If } e_1 \ e_2 \ e_3) \quad \mapsto_{\mathcal{C}} \quad (\texttt{If } \square \ e_2 \ e_3) \triangleright s\succ e_1$ 



Refinement and Simulation

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$$(\text{If} \square e_2 e_3) \triangleright s \prec \text{True} \quad \mapsto_C \quad s \succ e_2$$

$$(\text{If} \square e_2 e_3) \triangleright s \prec \text{False} \quad \mapsto_C \quad s \succ e_3$$

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### **Functions**

Recfun (here abbreviated to Fun) evaluates to a function value:

 $s \succ (\operatorname{Fun} (f.x. e)) \mapsto_C s \prec \langle\!\langle f.x. e \rangle\!\rangle$ 



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Function application is then handled similarly to Plus.

 $s \succ (\text{Apply } e_1 \ e_2) \quad \mapsto_C \quad (\text{Apply } \Box \ e_2) \triangleright s \succ e_1$ 

Refinement and Simulation

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 $(\texttt{Apply} \ \Box \ e_2) \triangleright s \prec \langle\!\langle f.x. \ e \rangle\!\rangle \quad \mapsto_C \quad (\texttt{Apply} \ \langle\!\langle f.x. \ e \rangle\!\rangle \ \Box) \triangleright s \succ e_2$ 

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 $(\text{Apply } \langle\!\langle f.x. \ e \rangle\!\rangle \square) \triangleright s \prec v \quad \mapsto_C \quad s \succ e[x := v, f := (\text{Fun } (f.x.e))]$ 

We are still using substitution for now.

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### What have we done?

- All the rules are axioms we can now implement the evaluator with a simple while loop (or a *tail recursive* function).
- We have a lower-level specification helps with code generation (e.g. in an assembly language)
- Substitution is still a machine operation we need to find a way to eliminate that.

Refinement and Simulation

### Correctness

While the M-Machine is reasonably straightforward definition of the language's semantics, the C-Machine is much more detailed.

We wish to prove a theorem that tells us that the C-Machine behaves analogously to the M-Machine.



# Correctness

While the M-Machine is reasonably straightforward definition of the language's semantics, the C-Machine is much more detailed.

We wish to prove a theorem that tells us that the C-Machine behaves analogously to the M-Machine.

#### Refinement

A low-level (*concrete*) semantics of a program is a *refinement* of a high-level (*abstract*) semantics if every possible execution in the low-level semantics has a corresponding execution in the high-level semantics. In our case:

$$\forall e, v. \frac{\circ \succ e \quad \stackrel{\star}{\mapsto}_{C} \quad \circ \prec v}{e \quad \stackrel{\star}{\mapsto}_{M} \quad v}$$

Functional correctness properties are preserved by refinement, but security properties are not.

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# How to Prove Refinement

We can't get away with simply proving that each C machine step has a corresponding step in the M-Machine, because the C-Machine makes multiple steps that are no-ops in the M-Machine:

 $\circ \succ (+ (+ (N 2) (N 3)) (N 4))$ (+ (+ (N 2) (N 3)) (N 4)) $\mapsto_{\mathcal{C}}$  (+  $\square$  (N 4))  $\triangleright \circ \succ$  (+ (N 2) (N 3))  $\mapsto_{\mathcal{C}}$  (+  $\square$  (N 3))  $\triangleright$  (+  $\square$  (N 4))  $\triangleright \circ \succ$  (N 2)  $\mapsto_{\mathcal{C}}$  (+  $\square$  (N 3))  $\triangleright$  (+  $\square$  (N 4))  $\triangleright \circ \prec 2$  $\mapsto_{\mathcal{C}}$  (+ 2  $\square$ )  $\triangleright$  (+  $\square$  (N 4))  $\triangleright$   $\circ$   $\succ$  (N 3)  $\mapsto_{C}$  (+ 2  $\square$ )  $\triangleright$  (+  $\square$  (N 4))  $\triangleright \circ \prec 3$  $\mapsto_C$  (+  $\square$  (N 4))  $\triangleright \circ \prec 5$  $\mapsto_{M}$  (+ (N 5) (N 4))  $\mapsto_{\mathcal{C}}$  (+ 5  $\square$ )  $\triangleright \circ \succ$  (N 4)  $\mapsto_{\mathcal{C}}$  (+ 5  $\square$ )  $\triangleright \circ \prec 4$  $\mapsto c \circ \prec 9$  $\mapsto_M$  (N 9)

# How to Prove Refinement

- Define an *abstraction function*  $\mathcal{A} : \Sigma_C \to \Sigma_M$  that relates C-Machine states to M-Machine states, describing how they "correspond".
- Prove, for all initial states *σ* ∈ *I<sub>C</sub>*, that the corresponding state *A*(*σ*) ∈ *I<sub>M</sub>*.

**③** Prove for each step in the C-Machine  $\sigma_1 \mapsto_C \sigma_2$ , either:

- the step is a no-op in the M-Machine and  $\mathcal{A}(\sigma_1) = \mathcal{A}(\sigma_2)$ , or
- the step is replicated by the M-Machine  $\mathcal{A}(\sigma_1) \mapsto_M \mathcal{A}(\sigma_2)$ .
- Prove, for all final states  $\sigma \in F_C$ , that  $\mathcal{A}(\sigma) \in F_M$ .

In general this abstraction function is called a *simulation relation* and this type of proof is called a *simulation* proof.

# **The Abstraction Function**

Our abstraction function  $\mathcal{A}$  will need to relate states such that each transition that corresponds to a no-op in the M-Machine will move between  $\mathcal{A}$ -equivalent states:



# **Abstraction Function**

Given a C-Machine state with a stack and a current expression (or value), we reconstruct the overall expression to get the corresponding M-Machine state.

By definition, all the initial/final states of the C-Machine are mapped to initial/final states of the M-Machine. So all that is left is the requirement for each transition.

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**Showing Refinement for Plus** 

 $s \succ (\texttt{Plus } e_1 \ e_2) \quad \mapsto_{\mathcal{C}} \quad (\texttt{Plus } \Box \ e_2) \triangleright s \succ e_1$ 



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## **Showing Refinement for Plus**

$$s \succ (\texttt{Plus } e_1 \ e_2) \quad \mapsto_{\mathcal{C}} \quad (\texttt{Plus } \Box \ e_2) \triangleright s \succ e_1$$

This is a no-op in the M-Machine:

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**Showing Refinement for Plus** 

 $(\texttt{Plus} \Box e_2) \triangleright s \prec v_1 \quad \mapsto_{\mathcal{C}} \quad (\texttt{Plus} v_1 \Box) \triangleright s \succ e_2$ 



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## **Showing Refinement for Plus**

#### $(\texttt{Plus} \Box e_2) \triangleright s \prec v_1 \quad \mapsto_{\mathcal{C}} \quad (\texttt{Plus} v_1 \Box) \triangleright s \succ e_2$

Another no-op in the M-Machine:

# **Showing Refinement for Plus**

#### $(\texttt{Plus } v_1 \Box) \triangleright s \prec v_2 \quad \mapsto_{\boldsymbol{C}} \quad s \prec v_1 + v_2$

This corresponds to a M-Machine transition:

Technically the reduction step (\*) requires induction on the stack.